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	Application Number	To Be Assigned <i>10/810525</i>
	Filing Date	Herewith
	First Named Inventor	Kiyotaka Mori
	Art Unit	To Be Assigned
	Examiner Name	To Be Assigned
SHEET 2 of 2		Attorney Docket No. MATB-406US

NON-PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
<i>OK</i>		AIST Announces World's Thinnest Vertical-Type Double-Gate MOSFET Using Newly Discovered Process, AIST Today International Edition No. 8, pp. 10-12, 2003.	<input type="checkbox"/>
<i>Ch</i>		MASSIMO LAZZAIR ET AL., Block Copolymers as a Tool for Nanomaterial Fabrication, Advanced Materials 15, No. 19, pp. 1583-1594, October 2, 2003.	<input type="checkbox"/>
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

<sup>1</sup>Applicant's unique citation designation number (optional).

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